

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	30273	(semiconductor or integrated adj circuit) and socket	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:08
L2	15361	1 and base	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:08
L3	1193	2 and lid	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:10
L4	324	3 and hinge\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:11
L5	21	4 and lock\$4 near mechanism	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:11
L6	15	5 and rotat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:15
L7	1	6 and inclin\$4 near surface\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 13:14